

LISTA DE LUCRĂRI – Prof.dr.ing. Corneliu Marinov

A – teza de doctorat

Title: “*Contributii la Metodele de analiza ale circuitelor electrice neliniare . Timpul de comutatie in circuite cu dispozitive semiconductoare.*” UPB 1977 .

Adviser: Prof. I.S. Antoniu

B – Carti si capitole in carti

1. C. A. Marinov. Introduction to Signal Analysis, Circuits and Systems, Lecture Notes, Jyväskylä University, Finland, 1998.
2. C. A. Marinov and P. Neittaanmaki. Mathematical Models in Electrical Circuits: Theory and Applications, Kluwer Academic Publishers, Dordrecht, Boston 1991.
Referred in:
 - A. H. Zemanian, Book review, in Bull. Amer. Math. Soc, vol. 26, no.1, Jan., Pages 194-198, 1993.
 - P. R. Shepherd, Book review, in Microelectronics Journal, vol. 1, 1992.
3. C. A. Marinov. Applications of Dissipative Operators in Electrical Circuit Theory, Lecture Notes, Lappeenranta University of Technology, Finland, 1985.
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C – Lucrari indexate ISI/BDI

1. R. L. Costea and C.A. Marinov, “*New accurate and flexible design procedure for a stable KWTA continuous time network*”, IEEE Transactions on Neural Networks, Vol. 22, No.9, pp.1357-1367, 2011.
2. C. A. Marinov and R. L. Costea, “*Time-Oriented Synthesis for a WTA Continuous Time Neural Networks Affected by Capacitive Cross-Coupling*”, IEEE Transactions on Circuits and Systems I: Regular Papers, Vol. 57, No. 6, pp. 1358-1370, June 2010
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4. C.A. Marinov and J.J.Hopfield. Stable Computational Dynamics for a Class of Circuits with $O(N)$ Interconnections Capable of KWTA and Rank Extractions. *IEEE Transactions on Circuits and Systems.Part I :Regular Papers*, Vol 52, No.5, Pages 949-959, 2005.
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Conference Papers:

1. R. L. Costea and C. A. Marinov, “*Continuous time recurrent neural network designed for KWTA operation*”, 2011 International Joint Conference on Neural Networks – IJCNN 2011, pp. 86-89, San Jose, USA.
2. R. L. Costea and C. A. Marinov, “*Recurrent neural networks as a KWTA selector: a synthesis procedure*”, IEEE International Symposium on Circuits and Systems - ISCAS 2011, pp.1093-1096, Rio de Janeiro, Brazilia, 2011.
3. R. L. Costea and C. A. Marinov, “*K-WTA Selection Using a Recurrent Neural Network*”, Proceedings of Fourteenth International Conference on Cognitive and neural Systems, May 19-22, 2010, Boston, USA.
4. R. L. Costea and C.A. Marinov, “*Time evaluation for WTA Hopfield type circuits affected by cross-coupling capacitances*”, in M. Koppen et al. (EDS), ICONIP 2008, Part II, LNCS 5507, p. 885-892, Springer-Verlag Berlin Heidelberg, 2009.
5. R. L. Costea and C. A. Marinov, “*Speed and correctness in computational neural circuits*”, Proceedings of Thirteenth International Conference on Cognitive and neural Systems, pp.128, May 27-30, 2009, Boston, USA.
6. R. L. Costea and C. A. Marinov, “*Time evaluation for Hopfield type circuits affected by cross-coupling capacitances*”, 15th Internatinal Conference, ICONIP 2008, pp. 82-83, Nov. 2008, Auckland, New Zealand.
7. R. L. Costea and C. A. Marinov, “*Time-problem in Hopfield neural networks with parasitic capacitances*”, International Symposium on Electronics and Telecommunication ETC’08, Eight Edition, 25-26 September, 2008, Timisoara, Romania. Published in “*Buletinul Stiintific al Universitatii Politehnica din Timisoara*”, Transactions on Electronics and Communcations, Tom 53(67), Fascicola 1, 2008.

8. R. L. Costea and C. A. Marinov, "A neural maximum selector: explicit parameters set-up for time performance", The 5th International Mediterranean and Latin American Modeling Multi-Conference I3M 2008, The European Modeling & Simulation symposium EMSS 2008, pp. 348-352, September 17-19, 2008, Campora S. Giovanni, Italy, ISSN 978-88-903724-0-7.
9. R. L. Costea and C. A. Marinov, "Correct behavior and processing time for a WTA neural network under the influence of coupling capacitances", Proceedings of Twelfth International Conference on Cognitive and neural Systems, pp.117, May 14-17, 2008, Boston, USA.
10. R. L. Costea and C. A. Marinov, "Clocking and WTA design of a continuous time Hopfield net with parasitic capacitances", European Conference on Circuit Theory and Design, ECCTD2007, pp. 396-399, Seville, 26-30 August 2007, IEEE Catalog number 07EX1835C, ISBN 1-4244-1342-7.
11. R. L. Costea and C. Marinov, "Clocking a WTA network under capacitive coupling", International Symposium on Signals Circuits and Systems, ISSCS, pp. 273-274, Vol. 1, ISSCS, Iasi, 2007, IEEE Catalog number 07EX1678, ISBN 1-4244-0968-3.
12. R. L. Costea and C.A. Marinov, "The impact of capacitive faults on WTA performances", The 6-th International Conference on Scientific Computing in Electrical Engineering - SCEE2006, pp.148-149, Sinaia, Romania, September 2006.
13. R. L. Costea and C. A. Marinov, "Processing time and cross capacitive coupling for a Winner Take All circuit", International Conference Mixed Design of Integrated Circuits and Systems, Gdynia, Poland, pp.518-521, June 2006.
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25. P. N. Shivakumar, J. J. Williams, Q. Ye, and C. A. Marinov. On two sided bounds related to weakly diagonally dominant M matrices with application to digital circuit dynamics, *Int. Congress Indust. and Appl. Math.*, Hamburg, July, 1995.
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